

100

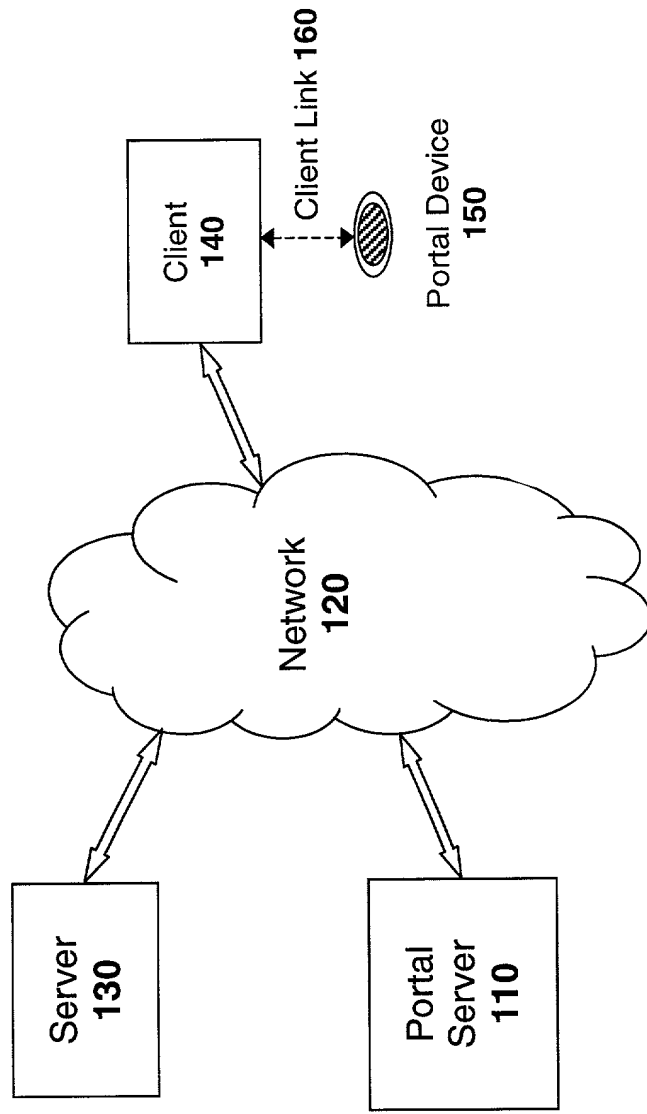


FIG. 1

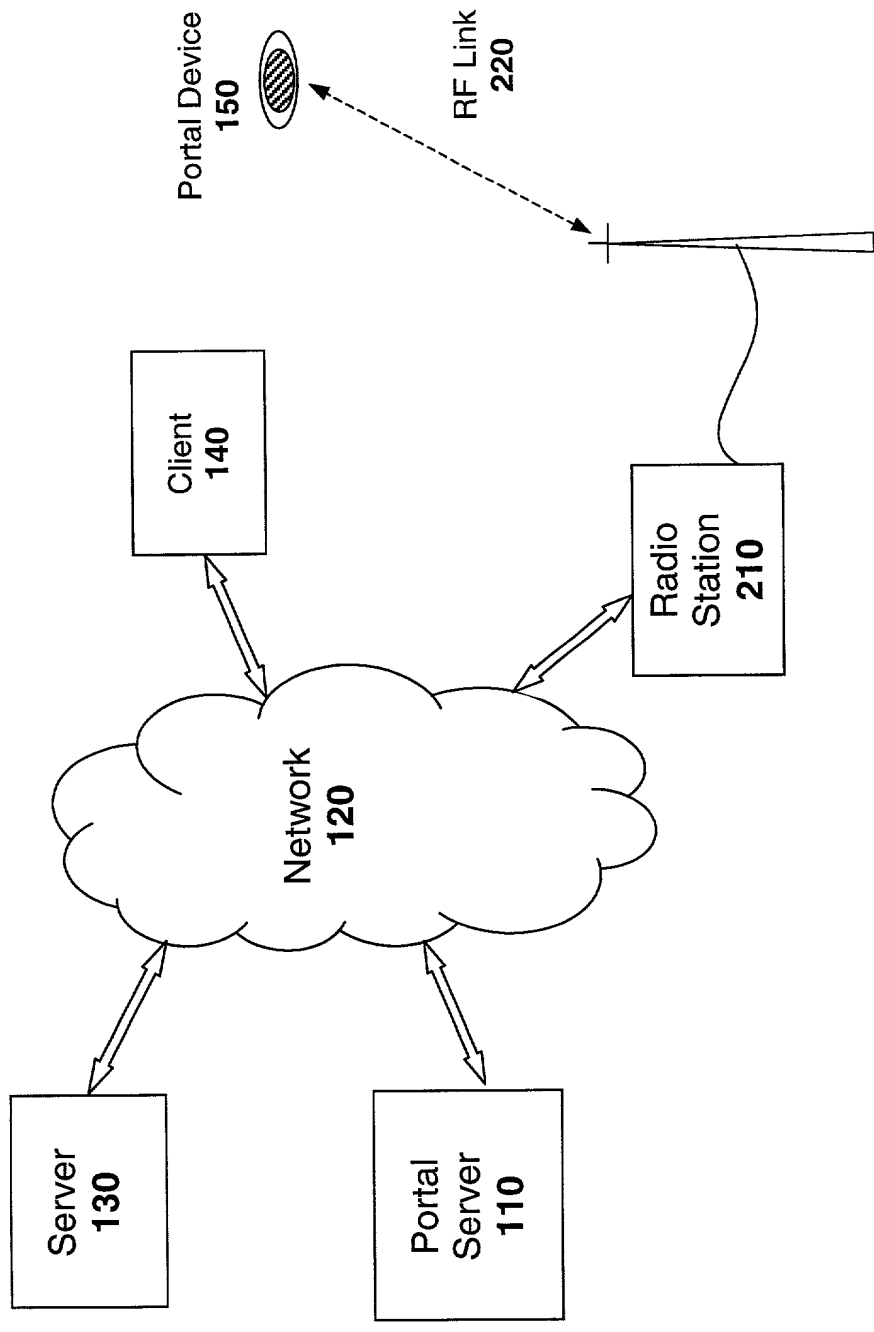


FIG. 2

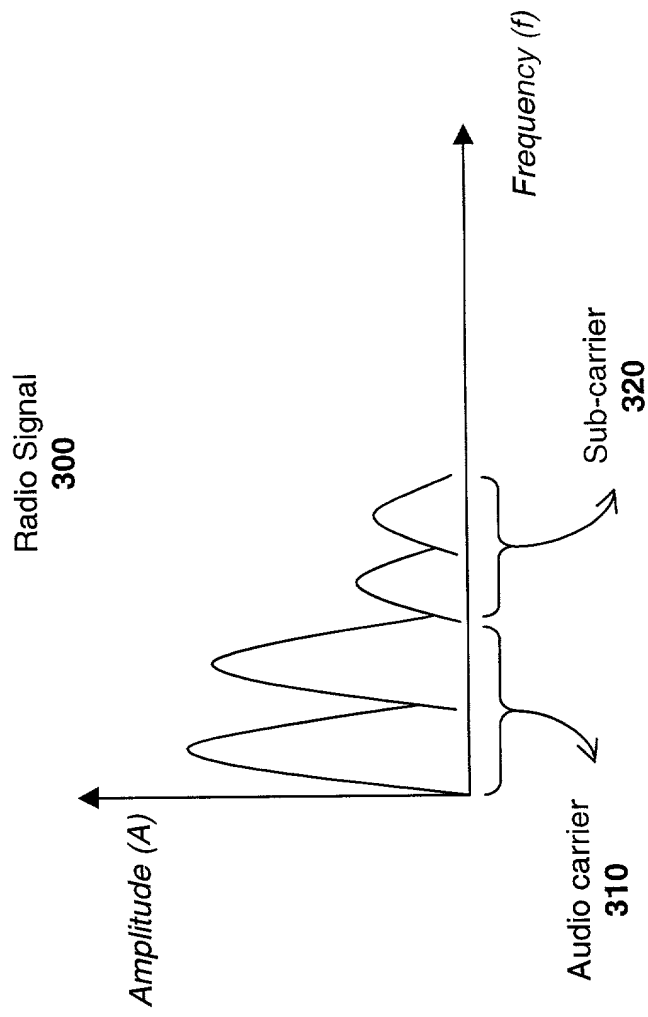


FIG. 3

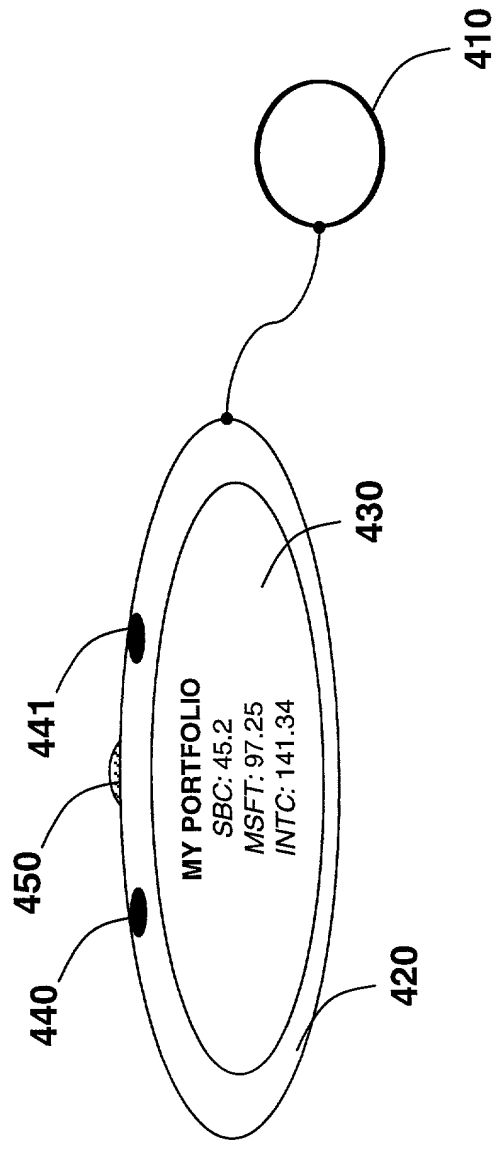


FIG. 4

FIG. 5 is a block diagram of a system 500 in accordance with one embodiment of the present invention. The system 500 includes a communication interface 570, a microcontroller 505, a battery 560, a display controller 575, an LCD 576, external memory 550, a CPU 510, a ROM 570, an interpreter 520, a toolbox 530, a scratchpad RAM 540, and microprograms &/or portal data 565.

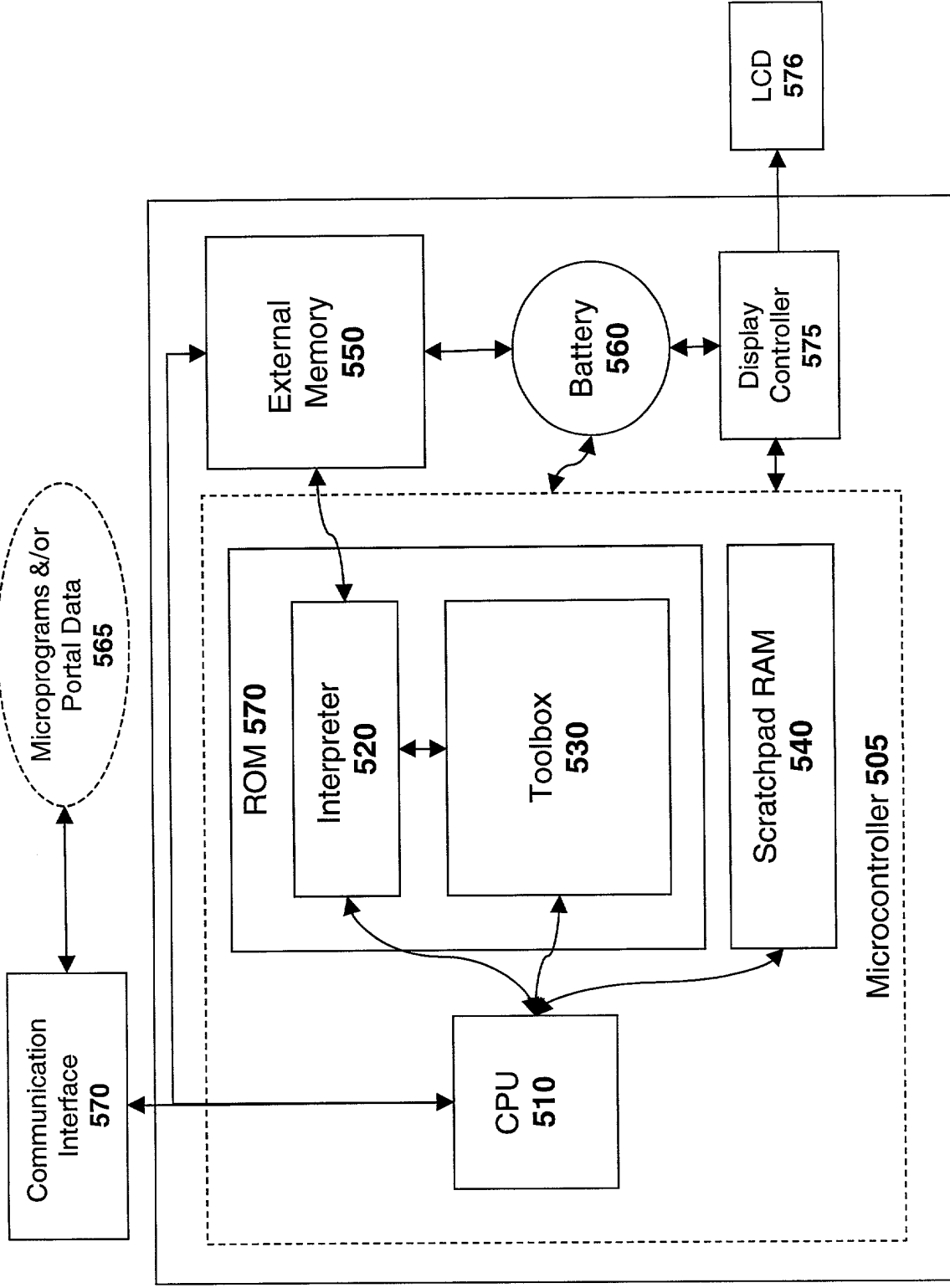


FIG. 5

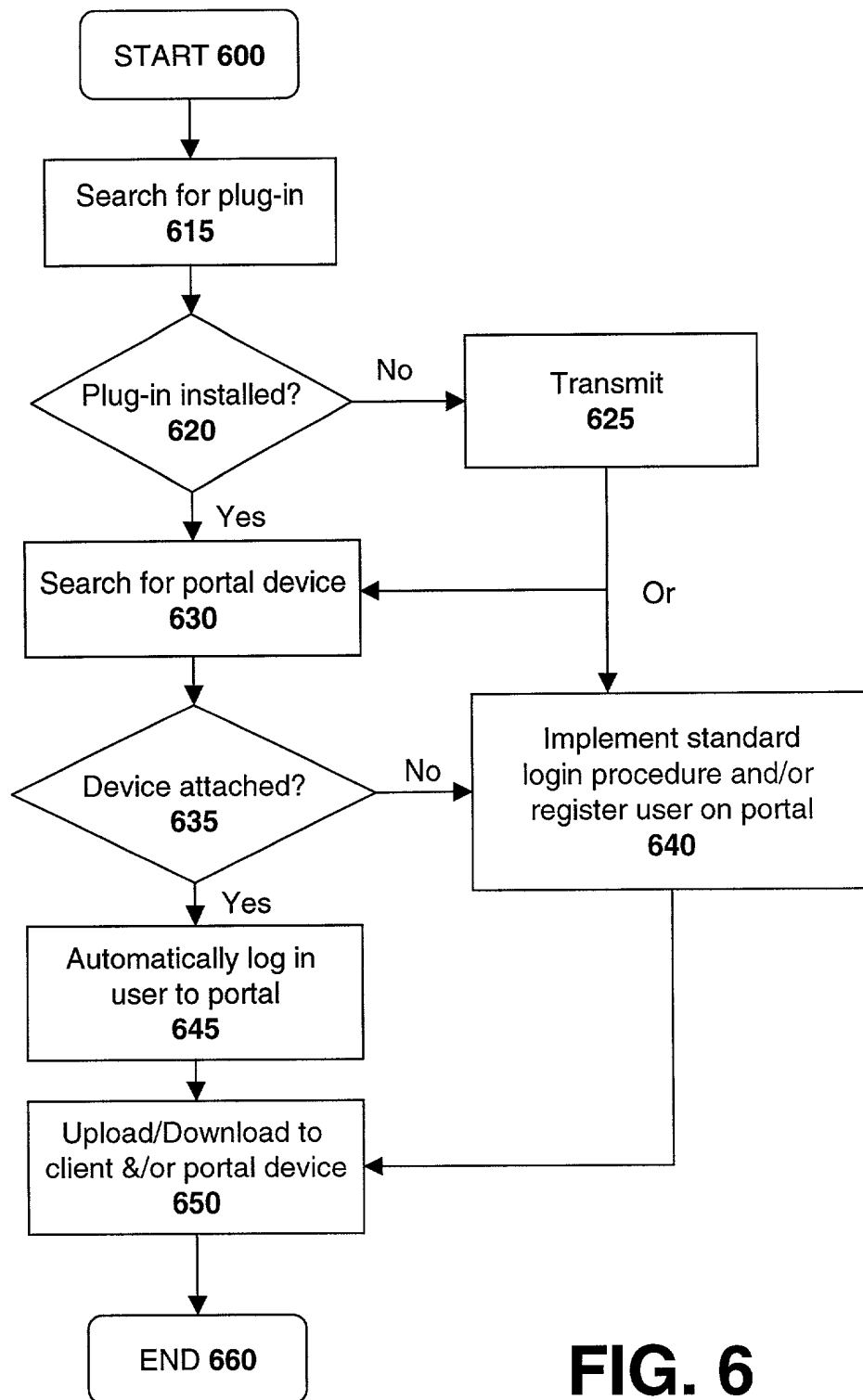


FIG. 6

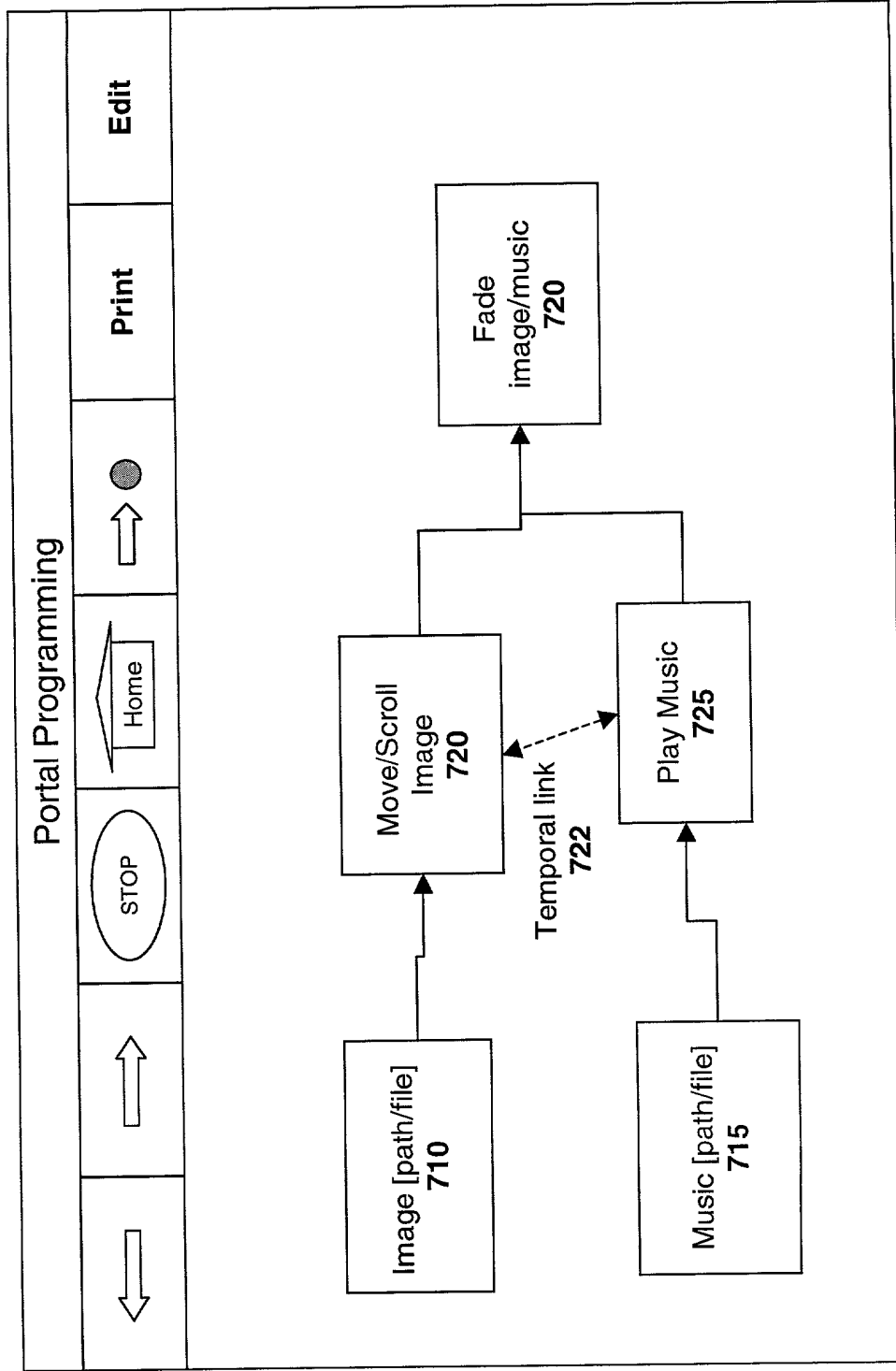


FIG. 7

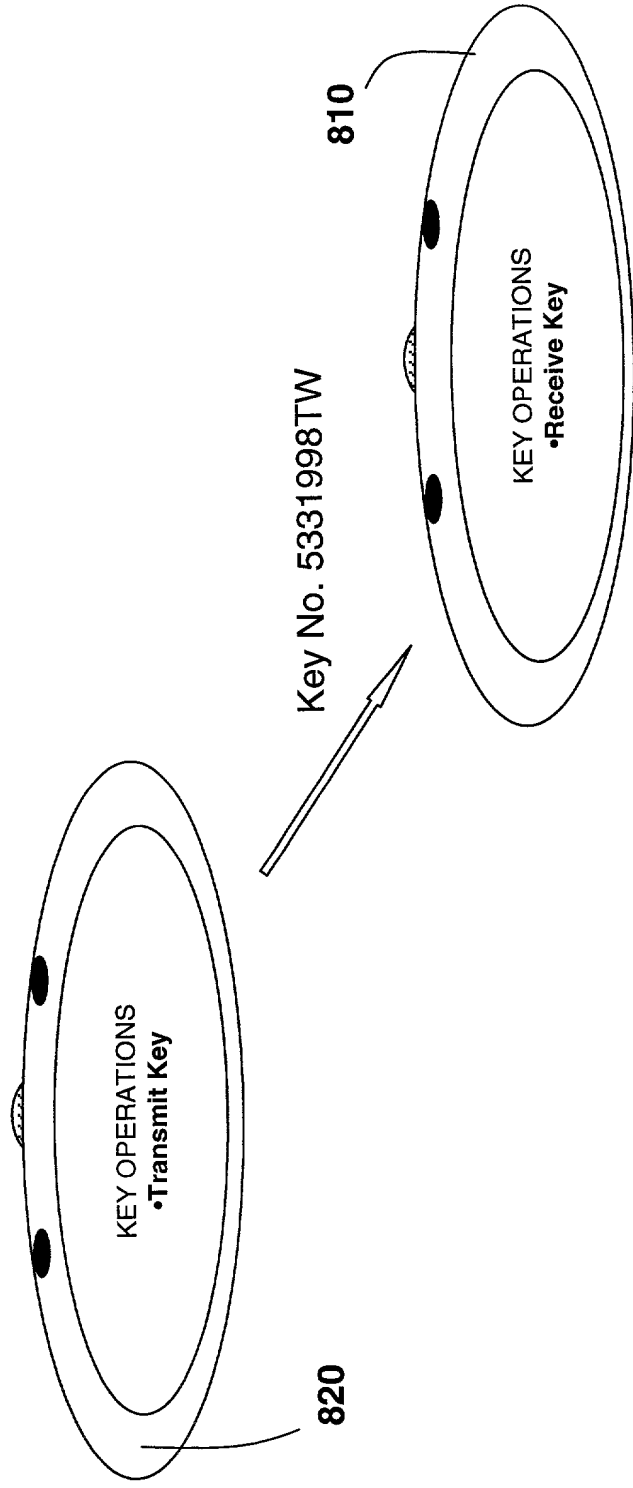


FIG. 8

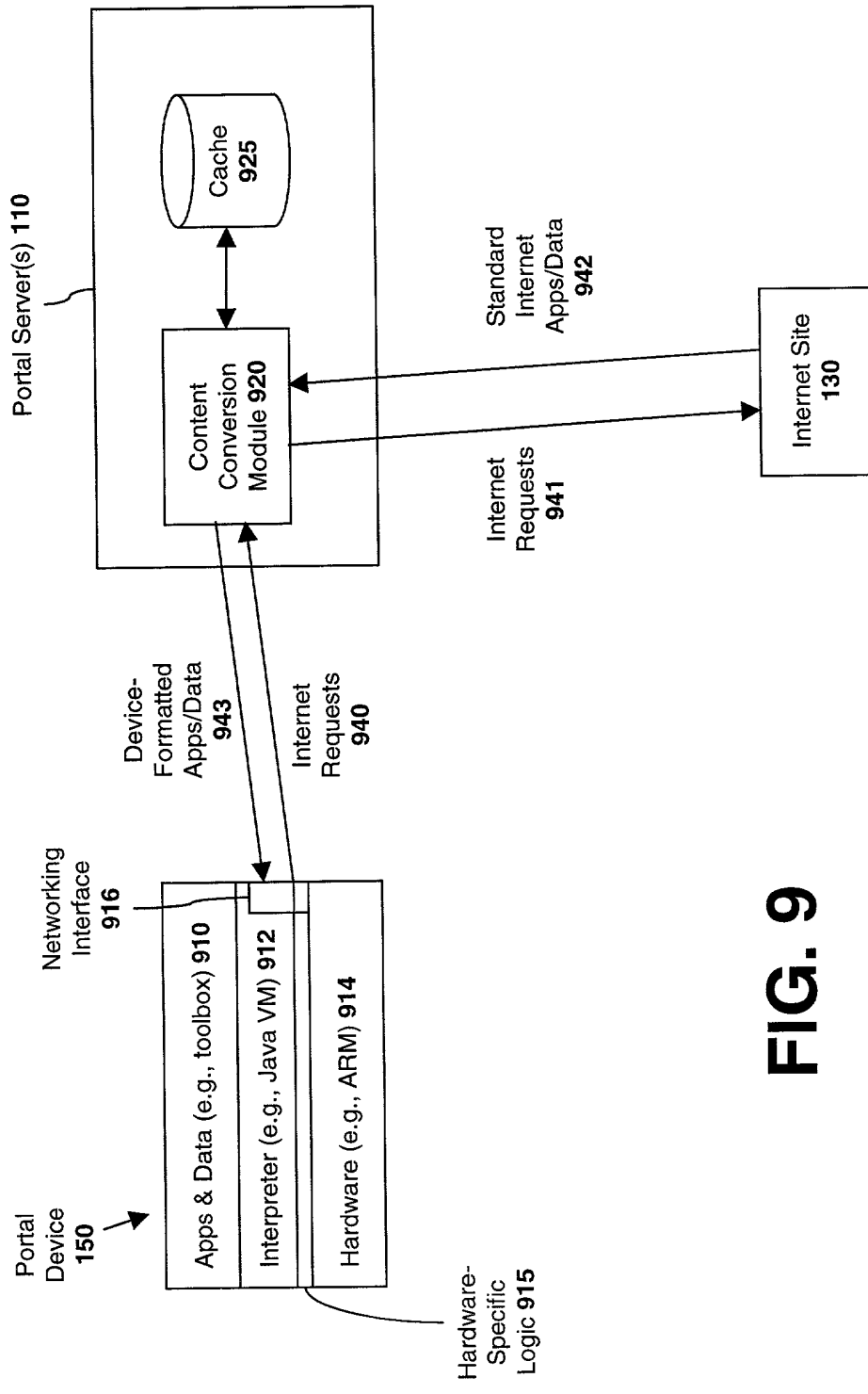


FIG. 9

Transmission from Caller B
(e.g., email, telephone call)

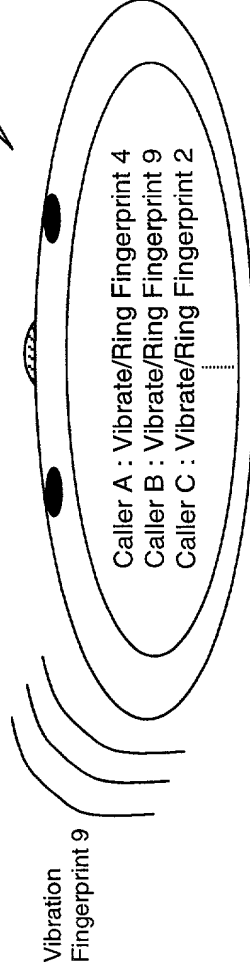


FIG. 10